San Francisco State University
School of Engineering

Course Outline for ENGR 301 Electrical Measurements

Bulletin Description:
301 Electrical Measurements (1) F,S
Prerequisite: ENGR 300 and 353 (may be taken concurrently). Measurement techniques, device characterization, experimental verification, and PSpice simulation. 2nd-order transient and frequency responses. Characterization of diodes, BJTs and FETs. Diode circuits, transistor amplifiers, simple logic gates. Laboratory. Extra fee required.

Textbook:
Lab Manual for ENGR 301(available on web, .Franco, Sergio and Klingenberg, Larry
Electronic Devices (Conventional Flow Version) by Thomas L., Floyd, 8th edition, Prentice Hall.

References:
1. B. Razavi.: Fundamentals of *Microelectronics*, Wiley 2008.
2. Joseph G. Tront.: *PSPICE for Basic Microelectronics*, McGraw-Hill, 2008.

Coordinator:
Hao Jiang, Associate Professor of Electrical Engineering

Prerequisites by Topic:
1. Ability to use standard instrumentation such as multi-meters, oscilloscope, power supplies, and function/pulse generators, as gained in ENGR 206.
2. Ability to conduct experiments, perform laboratory measurements and plot/interpret experimental data, as gained in ENGR 300.
3. Ability to use PSpice for simple circuit simulations.
4. Ability to write laboratory reports, emphasizing technical merit as well as communication skills, both graphic and written.

Course Objectives:
1. To measure the characteristics of common electronic devices such as diodes, BJTs, FETs, and to compare with theoretical prediction. {A.1, B.1, B.2}*
2. To observe experimentally the behavior of the aforementioned devices in a variety of common applications, such as rectification, regulation, amplification, and digital logic, and to compare with theoretical prediction. {A.2, B.1, B.2}
3. To simulate the aforementioned circuits via PSpice, and to compare with experimental observations. {B.3}
4. To plot, analyze, and interpret data, and to prepare technical reports of appropriate quality. {B.1, B.2}

**Refers to School of Engineering desired outcome*

Topics:

1. Second-order step responses under various damping conditions; frequency responses, Bode Plots.
2. Diode characteristics, and basic diode applications as rectifiers and regulators.
3. Transistor (BJT and MOSFET) characteristics, and basic transistor applications as amplifiers and logic circuits.
4. Computer simulation of diode, and transistor circuits using PSpice; comparison with experimental observations.

Professional Component:

1. Engineering Sciences: 100%
2. Engineering Design: 0%

Evaluation

1. Lab Reports: 4×15=60 points (group based)
2. Quizzes: 4×5=20 points (individual based)
3. Projects: design 10 points; demonstration 10 points; (individual based)
   Total = 100 points

Grading Policy:

A (100 to 95)	A- (94 to 90)	B+ (89 to 86)	B (85 to 83)	B- (82 to 80)		
C+ (79 to 76)	C (75 to 73)	C- (72 to 70)	D+ (69 to 66)	D (65 to 63)	D- (62 to 60)	F below 60

Performance Criteria:

Objective 1

- 1.1 Students will demonstrate an ability to characterize junction diodes. [2,3,4], {A.1, B.1, B.2}
- 1.2 Students will demonstrate an ability to characterize bipolar transistors. [2,3,4], {A.1, B.1, B.2}
- 1.3 Students will demonstrate an ability to characterize field-effect transistors. [2,3,4], {A.1, B.1, B.2}

Objective 2

- 2.1 Students will be able to verify experimentally popular diode applications such as rectification and regulation, and compare with theoretical prediction. [2,3], {A.2, B.1, B.2}
- 2.2 Students will be able to verify experimentally popular BJT applications such as amplification and digital logic, and compare with theoretical predictions. [2,3], {A.2, B.1, B.2}
- 2.3 Students will be able to verify experimentally popular FET applications such as amplification and digital logic, and compare with theoretical predictions [2,3], {A.2, B.1, B.2}

Objective 3

- 3.1 Students will demonstrate a skill to use PSpice to simulate the transient and frequency responses of a second-order circuit, and compare with experimental observations. [4], {B.3}
- 3.2 Students will demonstrate a skill to use PSpice to simulate the diode circuits investigated in the lab, and compare with measured data. [4], {B.3}

3.3 Students will demonstrate a skill to use PSpice to simulate the BJT and MOSFET amplifiers investigated in the lab, and compare with measured data. [4], {B.3}

3.4 Students will demonstrate a skill to use PSpice to simulate the BJT and MOSFET logic circuits investigated in the lab, and compare with measured data. [4], {B.3}

Objective 4

4.1 Students will demonstrate an ability in collecting, plotting, and interpreting experimental data, comparing with theoretical predictions, and accounting for discrepancies. [1,2], {B.1, B.2}

4.2 Students will demonstrate a skill in the presentation of experimental results via effective graphic means, such as *i-v* characteristics, Bode Plots, voltage transfer curves, and waveforms. [1,2], {B.1, B.2}

4.3 Students will demonstrate a skill in technical report preparation emphasizing both technical merit and effective writing. [1,2], {B.1, B.2}

*Numbers in brackets [] refer to evaluation method
**Related School outcomes {}

Instructor

Instructor: Hao Jiang; Email: jianghao@sfsu.edu;
Office: SCI 213C Office Hrs: (MWF 10am~11am) or by appt.
Phone: 338-6379

Class/Laboratory Schedule:

One 3-hour lab/week
Fee required ($23 each student) paid as $46 per two-man team. Fee includes components kit, breadboard, miscellaneous components, and printed circuit board manufacturing. Late circuit boards are not covered by fee. No refunds.

Scheduled Experiments:

1. Design and Build Circuit Board Project
2. Time and Frequency Response of Series *RLC* Circuits
3. Diode Characteristics and Applications
4. BJT Characteristics and Applications
5. MOSFET Characteristics and Applications

Notes on Prerequisites:

Engineering students must have a copy of the course approval form on file. Non-engineering students must submit a copy of the grade report showing the appropriate course grades for ENGR 205 and 206. Students who drop ENGR 353 need to drop ENGR 301.

Relationship to Other Courses:

This course extends the introductory laboratory practices of ENGR 206 to a more advanced level, focusing on the electronic devices studied in ENGR 353, which students are required to take concurrently or prior to ENGR 301. ENGR 301 also prepares the student for ENGR 442, 445, 453, and 455, to which it is a required prerequisite.

About Lab Reports:

1. It should be neat and clear.
2. DO NOT simply copy the “objective” and “procedure”. Use your own words.
3. Label all the plots, figures and tables, for example, “Collector Current of a BJT versus Collector Voltage” is much better than “I vs. V”.
4. Be sensitive to linear or log scale in the plots.
5. Clearly indicate the unit, ‘mV’ or ‘V’.
6. Show the derivation in the calculation, DO NOT simply put out a number.

A well polished engineering report also reflects the engineering quality.